

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10017883	FILING DATE 12/13/2001	CLASS 379	SUBCLASS 399.01	GAL 2644 2645	EXAMINER TRAN
**APPLICANTS: Chen Robert; Gammel John; Spires Dewayne; BZINGY (3)					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO		
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no	Chen 10-13-24		
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no			
Verified and Acknowledged Examiner's initials					
TITLE : Frequency-dependent impedance synthesis for DSL interface circuits					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED		
		Assistant Examiner		Total Claims
				Print Claim for O.G.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.	Figs.Drwg.	Print Fig.
<input type="checkbox"/> TERMINAL		Primary Examiner		
DISCLAIMER		Application Examiner		
<p>PREPARED FOR ISSUE</p> <p>WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.</p>				

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